

Want to do some *real engineering* again?

Job: Analog Design Engineers with NFC experience
Location: Graz Austria
Start Date: Q2 2019

Overview

Panthronics AG an independent IC company with expertise in high performance NFC/RFID hardware and software solutions is anticipating the forthcoming launch of the PTX range of differentiated NFC products. We are currently looking for outstanding people in this domain to strengthen our team in Graz. We now require additional experienced **Analog Design Engineers** for the development of the analog building blocks of ICs for RFID/NFC, Mobiles, Wearables, and IoT-Applications.

This is an exciting opportunity to join a first-class team and gain a meaningful equity position within a fast-growing company.

Job Description

- Define design concept; transistor-level design and verification
- Floor planning, pinning, top-level evaluation
- Modeling of analog circuits by means of Verilog or VHDL; Design for testability
- Technical communication with customer; strong interaction with off-site design teams
- Design and Layout reviews; layout-support
- Lab-evaluation and characterization

Job Profile

- Degree in the field of electronic engineering
- At least 8 years' experience in analog circuit design for RFID readers and/or NFC controllers
- Strong knowledge in analog design flow and development tools such as Cadence ADE
- Fluent in English – German skills are a plus

Compensation

- Salary commensurate with market based on individual person
This position is in accordance with the Austrian collective agreement for employees within engineering companies, employment group V: www.ingenieurbueros.at/verband/de/kollektivvertrag.
- Generous Stock Incentive program
- Variable incentive Scheme

Contact

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